

Our Docket No: 42P13230D

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:)		
)		
Shafquat Ahmed et al.)	Examiner:	Not yet assigned
)		
Serial No: Not yet assigned)	Art Unit:	Not yet assigned
)		
Filed: Concurrently Herewith)		
)		
For: Method of Forming Transistor Having)		
<u>Insulating Spacers On Gate Sidewalls</u>)		
)		
A Rule 1.53(b) Divisional Application of)	Examiner:	Soward, Ida M.
Serial No.: 10/247,027)	Art Unit:	2822
Filed: September 19, 2002)		
)		

PRELIMINARY AMENDMENT

Mail Stop Patent Application
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

Prior to examination of the present Rule 1.53(b) divisional application, please enter the following amendments and consider the following remarks.

EXPRESS MAIL CERTIFICATE OF MAILING

Express Mail" mailing label number EV 331619636 US

I hereby certify that I am causing the above-referenced correspondence to be deposited with the United States Postal Service "Express Mail Post Office to Addressee" service on the date indicated below and that this paper or fee has been addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

July 15, 2003

Date of Deposit

Krista Mathieson

Name Of Person Mailing Correspondence


Signature

July 15, 2003

Date Signed